

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~striketrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please AMEND claim 1 in accordance with the following:

1. (CURRENTLY AMENDED) An electronic apparatus to which a plurality of batteries are detachably mounted, comprising:
 - a removal requirement receipt section receiving a removal requirement for a part of the mounted batteries;
 - a processing ability determination section responsive to the removal requirement for the part of the mounted batteries from said removal requirement receipt section to determine whether a supplying possible electric power from the remaining batteries is an electric power capable of maintaining a processing ability or an electric power only capable of maintaining a lowered processing ability; and
 - a processing ability control section lowering the processing ability when the part of the mounted batteries is removed, while keeping the electronic apparatus operative in accordance with a decision from said processing ability determination section that the electric power is only capable of maintaining a lowered processing ability.
2. (CANCELLED)
3. (PREVIOUSLY PRESENTED) An electronic apparatus to which a plurality of batteries are detachably mounted, comprising:
 - a mounting and removal detection section detecting mounting and removal of batteries; and
 - a processing ability control section responsive to a detection of a removal of a battery by said mounting and removal detection section to lower a processing ability when the part of the mounted batteries is removed while keeping the electronic apparatus operative.
4. (ORIGINAL) An electronic apparatus according to claim 1, in which said

electronic apparatus has a portion receiving a clock and operative in synchronism with the clock while consuming an electronic power according to a repetitive frequency of the clock,

wherein said processing ability control section changes over the frequency of the clock to control the processing ability.

5. (CANCELLED)

6. (ORIGINAL) An electronic apparatus according to claim 3, in which said electronic apparatus has a portion receiving a clock and operative in synchronism with the clock while consuming an electronic power according to a repetitive frequency of the clock,

wherein said processing ability control section changes over the frequency of the clock to control the processing ability.

7. (ORIGINAL) An electronic apparatus according to claim 1, wherein said processing ability determination section receives the removal requirement for a battery from said removal requirement receipt section and determines whether an electric power supplying ability is insufficient with only the remaining batteries, even if the processing ability is lowered by said processing ability control section, and

said electronic apparatus further comprises a removal acceptance display section for displaying inhibit or acceptance of the removal of a battery according as said processing ability determination section determines whether an electric power supplying ability is insufficient with only the remaining batteries, even if the processing ability is lowered by said processing ability control section.

8. (CANCELLED)

9. (PREVIOUSLY PRESENTED) An electronic apparatus according to claim 1, further comprising a residual electric power monitor section monitoring a residual electric power of the mounted batteries.

10. (CANCELLED)

11. (ORIGINAL) An electronic apparatus according to claim 9, wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries

and determines a residual electric power of the batteries through an arithmetic operation.

12. (CANCELLED)

13. (ORIGINAL) An electronic apparatus according to claim 1, wherein each of said batteries is a chargeable secondary battery provided in a battery pack, and a plurality of such battery packs are capable of being mounted on said electronic apparatus.

14. (CANCELLED)

15. (ORIGINAL) An electronic apparatus according to claim 3, wherein each of said batteries is a chargeable secondary battery provided in a battery pack, and a plurality of such battery packs are capable of being mounted on said electronic apparatus.

16. (PREVIOUSLY PRESENTED) An electronic apparatus according to claim 1, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing a residual electric power of a battery of an associated battery pack.

17. (CANCELLED)

18. (PREVIOUSLY PRESENTED) An electronic apparatus according to claim 9, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and

wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries referring to said memories.

19. (CANCELLED)

20. (PREVIOUSLY PRESENTED) An electronic apparatus according to claim 11, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery

packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and

wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries referring to said memories.

21. (CANCELLED)

22. (PREVIOUSLY PRESENTED) An electronic apparatus according to claim 1, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between a residual electric power of an associated battery and a maximum chargeable current, and

wherein said processing ability determination section performs a determination referring to said memories.

23. (PREVIOUSLY PRESENTED) An electronic apparatus according to claim 7, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between a residual electric power of an associated battery and a maximum chargeable current, and

wherein said processing ability determination section performs a determination referring to said memories.

24. (CANCELLED)

25. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus instructing an alteration of a processing ability to an electronic apparatus to which a plurality of batteries are detachably mounted, comprising:

a removal requirement receipt section receiving a removal requirement for a part of the batteries mounted on said electronic apparatus;

a processing ability determination section responsive to the removal requirement for the part of the batteries from said removal requirement receipt section to determine whether a

supplying possible electric power from the remaining batteries is an electric power capable of maintaining a processing ability or an electric power only capable of maintaining a lowered processing ability; and

a processing ability control section lowering the processing ability when the part of the mounted batteries is removed, while keeping the electronic apparatus operative in accordance with a decision from said processing ability determination section that the electric power is only capable of maintaining a lowered processing ability.

26. (CANCELLED)

27. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus instructing an alteration of a processing ability to an electronic apparatus to which a plurality of batteries are detachably mounted, comprising:

a mounting and removal detection section detecting mounting and removal of batteries on and from said electronic apparatus; and

a processing ability alteration instruction section responsive to a detection of a removal of part of the batteries a battery by said mounting and removal detection section to instruct said electronic apparatus to lower the processing ability when the part of the mounted batteries is removed, while keeping the electronic apparatus operative.

28. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 25, wherein said processing ability determination section receives the removal requirement for a battery from said removal requirement receipt section and determines whether an electric power supplying ability is insufficient with only the remaining batteries, even if the processing ability of said electronic apparatus is lowered, and

said processing ability alteration instruction apparatus further comprises a removal acceptance display section displaying inhibit or acceptance of the removal of a battery according as said processing ability determination section determines whether an electric power supplying ability is insufficient with only the remaining batteries, even if the processing ability is lowered.

29. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 26, further comprising:

a processing ability determination section responsive to the removal requirement for the part of the batteries from said removal requirement receipt section to determine whether an

electric power supplying ability is insufficient with only the remaining batteries, even if the processing ability of said electronic apparatus is lowered, and

a removal acceptance display section for displaying inhibit or acceptance of the removal of the part of the batteries according as said processing ability determination section determines whether an electric power supplying ability is insufficient with only the remaining batteries, even if the processing ability of said electronic apparatus is lowered.

30. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 25, further comprising a residual electric power monitor section monitoring a residual electric power of the mounted batteries.

31. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 26, further comprising a residual electric power monitor section monitoring a residual electric power of the mounted batteries.

32. (ORIGINAL) A processing ability alteration instruction apparatus according to claim 30, wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries through an arithmetic operation.

33. (ORIGINAL) A processing ability alteration instruction apparatus according to claim 31, wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries through an arithmetic operation.

34. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 25, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing a residual electric power of a battery of an associated battery pack.

35. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 26, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic

apparatus, and each of said battery packs has a memory storing a residual electric power of a battery of an associated battery pack.

36. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 30, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries referring to said memories.

37. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 31, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries referring to said memories.

38. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 32, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries referring to said memories.

39. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 33, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between voltage

and supplying current of an associated battery and a residual electric power of the battery, and wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries referring to said memories.

40. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 25, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between a residual electric power of an associated battery and a maximum chargeable current, and wherein said processing ability determination section performs a determination referring to said memories.

41. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 28, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between a residual electric power of an associated battery and a maximum chargeable current, and wherein said processing ability determination section performs a determination referring to said memories.

42. (PREVIOUSLY PRESENTED) A processing ability alteration instruction apparatus according to claim 29, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory storing an association between a residual electric power of an associated battery and a maximum chargeable current, and wherein said processing ability determination section performs a determination referring to said memories.